



**PATENT**  
**51270-277019**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**Nobuaki TSUJI, et al.**

Serial No.: 09/982,335

Filed: October 18, 2001

For: **SEMICONDUCTOR INPUT PROTECTION  
CIRCUIT**

Confirmation No.: 6722

Date of Notice of Allowance:  
July 28, 2004

Examiner: Ori Nadav

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: MAIL STOP ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 21, 2004.

By: 

Roger R. Wise, Reg. No. 31,204

**AMENDMENT AFTER ALLOWANCE PURSUANT TO 37 C.F.R. § 1.312**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowance dated July 28, 2004 in connection with the above-identified application, please enter the following Rule 312 amendments.

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